

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	(Jeffery near1 Steven near1 Beck or Matthew near1 Michael near1 Borg).in.	US-PGPUB; USPAT	OR	ON	2007/12/19 16:41
L2	9	(pixel and node and row and reset\$3 and voltage and chang\$3 and level and period).clm.	US-PGPUB; USPAT	OR	ON	2007/12/19 16:42

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S62	7	"3351503"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/14 11:19
S63	666	257/290.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/14 11:34
S64	1	("6211510").PN.	US-PGPUB; USPAT	OR	OFF	2007/12/16 17:06
S66	184	row near2 select\$3 with reset\$3 with (common\$4 or shar\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/16 17:11
S65	32	("3866067" "4395736" "5335015" "5471245").PN. OR ("6211510"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/16 17:11
S67	60	348/291.CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 10:24
S68	1222	257/292.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 11:46
S69	3826	250/208.1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 13:47
S1	3878	348/294-324.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 13:47
S70	720	358/482.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 15:30

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L13	2635	row same reset\$3 same (chang\$3 or puls\$3) same (high or low or period)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 15:58
L15	390	13 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 15:59
L14	28050	imag\$3 near2 sens\$3 same pixel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 15:59
L16	367	15 and ("348"/\$.ccls. or "257"/\$.ccls. or "358"/\$.ccls. or "250"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/19 16:02
L1	6	(Jeffery near1 Steven near1 Beck or Matthew near1 Michael near1 Borg). in.	US-PGPUB; USPAT	OR	ON	2007/12/19 16:41
L2	9	(pixel and node and row and reset\$3 and voltage and chang\$3 and level and period).clm.	US-PGPUB; USPAT	OR	ON	2007/12/19 16:42

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Active pixel image cell with embedded memory and pixel level ...

After **reset**, photodiode 120 begins accumulating **image** electrons. At the end of the signal integration **period**, **row** control line 128 is pulsed. ...

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Combined photogate and photodiode active pixel image sensor - US ...

However since for the photodiode active **pixel** 36 the diffusion node 115 is **reset** at the beginning of the integration **period**, the **reset** and signal **voltage** ...

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Active pixel image cell with embedded memory and pixel level ...

An active **pixel image** cell which includes a photosensor and an embedded memory ...

RESET. The **row** select **voltage** V.sub.RS causes the **voltage** on node 2, ...

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Combined photogate and photodiode active pixel image sensor ...

An active **pixel image** sensor includes an array of **pixels** arranged in a ... the beginning of the integration **period**, the **reset** and signal **voltage levels** are ...

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reset voltage VRS, thereby resetting the PG. Then, the **row** address Fossum, "Progress in CMOS active **pixel image** sensors," in Charge- ...

ieeexplore.ieee.org/iel1/16/9118/00405286.pdf?arnumber=405286 - [Similar pages](#)

(WO/2004/019605) A WIDE DYNAMIC RANGE LINEAR-AND-LOG ACTIVE PIXEL

The **pixel** circuit **reset voltage** VRST is produced by the output time **period** does not affect the **reset** operation, so that a stable **reset level** for the ...

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Standard CMOS Active Pixel Image Sensors for Multimedia ...

is common for all **pixels** within a **row** or a column of the **image** sensor array [3]. ... handles the **reset voltage level** and consists of an active transistor ...

doi.ieeecomputersociety.org/10.1109/ARVLSI.1995.515622 - [Similar pages](#)

[PDF] TIMING CONSIDERATIONS FOR THE TC211 FULL-FRAME CCD IMAGE SENSOR

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CCDimage sensor, which has a resolution of 165 **rows** by 192 **pixels**. the TCK-211 evaluation ... This is the point where the **reset voltage level** is sampled. ...

focus.ti.com/lit/an/soca011/soca011.pdf - [Similar pages](#)

Displays : Development of 3+2 dynamic drive scheme for cholesteric ...

The **row** driver IC has 3 **level** outputs of RH, RM (RH/2), and RL. And the ... The **pixel voltage levels** during the selection **period** have an ON and OFF state. ...

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Preliminary Demonstration of 1.0V CMOS Imager with Semi-Pixel ...

j-th **row** is shown in Fig. 2. A dark **level** of the photodiode **voltage** is determined in the **reset period**. In resetting, a **pixel**, **row** select and **reset** ...

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CMOS correlated-double-sampling and hold structure based on novel ...

With the CDSH procedure, the signal integration **voltage** $V_s = Q_s/C$ is compared with the 'dark' or 'empty' or 'reset' level **voltage** $V_r = Q_r/C$ when the ...
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11.19.98 Design Ideas

To **change** the scalar relationship between the controlling **voltage** applied to the power
Bit-map resolution (**pixels**), Bytes per **row**, Padded zeros per **row** ...
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An integrated 800x600 CMOS imaging system - Solid-State Circuits ...

tion **period** (Figure 17.3.4). The integration **period** for a **pixel row**. is. initiated by
resulting **voltage level** is again converted (7-8-. 9). **Change** ...
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Portable Design ARTICLE

The first **reset voltage** would be the largest **voltage level** as per the design ... To increase the dynamic range of an **image**, **pixels** are integrated two times, ...
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Awaiba - Your key to CMOS **image sensors** » Glossary

The blanking **level** is the nominal **voltage** of a video waveform during the ... Top; FLUSH **reset image sensor**: A high responsivity **pixel** architecture form ...
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A low-**voltage** PWM CMOS imager with small **pixel size** using an in ...

voltage operation and **pixel-level** A/D conversion are available. The **pixel** is ... end of the **pixel** readout, the **pixels** in the j-th **row** are **reset**. ...
joi.jlc.jst.go.jp/JST.JSTAGE/elex/4.271?from=Google - [Similar pages](#)

(WO/2006/138533) **PIXEL** INDIVIDUAL ANT-ECLIPSE CIRCUIT AND ITS ...

3A and also illustrates the **reset voltage** VRST **level** during operation of the FIG. 3A circuit. At time t0, **row** select signal SEL is applied to the **pixel** 100 ...
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voltage levels from internal memory into the DACs, then enables the DAC outputs to A **row** of **pixels** is **reset** by addressing the desired **row** and closing a ...
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[PDF] **27.3 - A 3 Megapixel, Low-Noise Flexible Architecture CMOS **Image** ...**

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this paper, a 3T **pixel image** sensor with low **reset** noise, good fill the end of the **reset period** so that the **reset** transistor is deep in ...
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International Journal of Mass Spectrometry : Active **pixel sensors** ...

Voltage level diagram showing **reset level** V3 and signal **levels** V1 and V2 as a ... During a **row** read cycle, **image** charge potentials were stored on the column ...
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[Preferences](#)Web Results 21 - 30 of about 184,000 for **pixel image row reset voltage level change period**. (0.09 seconds)**[PDF] OLED Driver Basic Register Setup Application Note AN001**File Format: PDF/Adobe Acrobat - [View as HTML](#)register settings with **Row Period** set to 46h P1 **period**, its charging **voltage level** is. always being **reset** during this **period**. ...catalog.osram-os.com/catalogue/catalogue.do;jsessionid=787F9ABE9D2C5C6B352B8EEC0C3FCE7B?act=downloadFile... -[Similar pages](#)**[PDF] LOCKHEED MARTIN**File Format: PDF/Adobe Acrobat - [View as HTML](#)CCD447. 2048 x 2048 **Pixel Image Area**. Full Frame CCD **Image Sensor** **reset** with. ϕ . R to a pre-charge **level** prior to the arrival of the next ...www.fairchildimaging.com/main/documents/CCD447DataSheet_003.pdf - [Similar pages](#)**[Sako et al., Improvements in Operating the Raytheon Array](#)**Horizontal lines in the top of the **image** are caused by bad **rows**. ... The **voltage level** of the signals changes after reading out a hot **pixel**. ...www.journals.uchicago.edu/cgi-bin/resolve?2003PASP..115.1407S - [Similar pages](#)**[PDF] CMOS ACTIVE PIXEL IMAGE SENSORS FOR HIGHLY INTEGRATED IMAGING SYSTEMS**File Format: PDF/Adobe Acrobat - [View as HTML](#)Furthermore, CCDS need many different **voltage levels** to reference for each column is obtained from the last **row** of **pixels** in the array, ...trs-new.jpl.nasa.gov/dspace/bitstream/2014/33217/1/94-1153.pdf - [Similar pages](#)**[PDF] An Introduction to Scientific Imaging Charge ...**File Format: PDF/Adobe Acrobat - [View as HTML](#)**pixel-by-pixel, row-by-row** representation. of the **image** falling on the CCD. through the CCD **reset period**. When the. output **voltage** has settled, ...www.site-inc.com/pdf/introdat.pdf - [Similar pages](#)**[Shift register circuit invention](#)**Each **row** of **pixels** shares a **row** conductor which connects to the gates of the thin ... This makes the circuit less sensitive to threshold **voltage levels** or ...www.freshpatents.com/Shift-register-circuit-dt20071025plan20070248205.php - 29k -Cached - [Similar pages](#)**[PDF] A CMOS image sensor for low light applications**File Format: PDF/Adobe Acrobat - [View as HTML](#)read out column-wise, the signal and **reset levels** of **pixels** in. the next **row** are ... **Change** in source **voltage** of a common gate transistor as a function ...www.isr.umd.edu/IBIS/publications/10.1109-ISCAS.2006.1692919.pdf - [Similar pages](#)**[Chapter 4 ACTIVE PIXEL SENSOR DESIGN: FROM PIXELS TO SYSTEMS](#)**enabling the **row** select transistor of the **pixel**. The CDS outputs the. difference between the **reset voltage level** and the photovoltage level. ...www.springerlink.com/index/n52k06572p03w534.pdf - [Similar pages](#)**[PDF] &**File Format: PDF/Adobe Acrobat - [View as HTML](#)**pixel circuit** 100 provides a **reset** signal V. RST. and a **pixel image** signal V ... **voltage level**. At the time t₂, the RST line and the feed-through ...<https://.../PublicationServer/router?iAction=3&cc=EP&pn=1532807&ki=B1> - [Similar pages](#)

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The **image** is scanned one **row** at a time, starting at the top of the sensor. The signal charge collected in the photodiodes in each **pixel** is **reset** as each **row** ...

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Active pixel sensor architectures in a-SiH for medical imaging ...

This work presents a **voltage** mediated active **pixel** sensor (APS) on-pixel readout circuit for ... the **RESET** TFT is switched OFF for an integration **period**, ...

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A 3-Pin 1.5 V 550 μ W 176 x 144 Self-Clocked CMOS Active Pixel ...

micropower CMOS active **pixel image** sensor are described in details in section 2. ... circuit adopted from [4] should increase the **pixel reset voltage** ...

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Molecular Expressions Microscopy Primer: Digital Imaging in ...

Reset noise can seriously limit the signal-to-noise ratio of the **image** sensor. ... When each integration **period** begins, all of the **pixels** in the same **row** ...

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A 128 \times 128-Pixel Artificial Retina LSI with Two-Dimensional ...

a **voltage level** of approximately 4 V by a **reset** pulse V_r . In the subsequent accumulation **period**, the photocurrent reduces the **ner** enables **image** ...

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EP809299 Eastman european software patent - Active pixel sensor ...

Pixels are arranged in an array (X-columns and Y-rows), to form an **image** sensor. Device operation occurs in the following manner. A given **row** is **reset** by ...

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(IUCr) Tests of a prototype pixel array detector for microsecond ...

Rapid imaging is accomplished by storing the integrated **voltage level** from **Image 8** is placed next to **image 1** to highlight the angular **change** in the ...

scripts.iucr.org/cgi-bin/paper?ay2005 - [Similar pages](#)

[PDF] STAR-1000 Datasheets

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This amplifier samples the output **voltage** and the **reset level** of the **pixel**, whose **row** is selected at that moment and presents these **voltage levels** to the ...

gwest.gats-inc.com/sofie/sun_sensor/star_1000_5.pdf - [Similar pages](#)

[PDF] Adaptive Sensitivity CCD Image Sensor ABSTRACT 1. INTRODUCTION

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whole array, but for each individual **pixel** that **period** is divided into two ... be **reset**. Those FFs for which both **row** and column lines are '1' are **reset**, ...

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[PDF] A 10 000 fps CMOS Sensor with Massively Parallel Image Processing

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in order to avoid any distortion due to a **row-by-row reset**. Cummings, "Cmos **pixel-level** adc with **change** detection," in Proceed- ...

vision.u-bourgogne.fr/le2i/user_data/publications/ieee_jssc07.pdf - [Similar pages](#)

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Pixim, Inc.

DPS uses this capability to sample the growing light intensity at each **pixel** many times during each **image** capture **period**. This allows exposure **level** of each ...

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nominal **voltage level** is 0.0V. The chip can be held in **reset** **pixels** are held at **reset**.

The second **row** contains **image** data from **row** 1 (Figure 5.1 and ...

www.sal.wisc.edu/whirc/archive/public/datasheets/raytheon/SB301UsersGuideRevB1.pdf - Similar pages

[DOC] CIRSI: the Cambridge Infrared Survey Instrument for Wide-Field ...

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This is repeated for each **row** in the quadrant giving a dataset consisting of 512 **rows** each of which is 1024 **pixels** wide. The **reset** corrected **images** are ...

www.ast.cam.ac.uk/~optics/papers/CIRSI_SPIE_4008_146.doc - Similar pages

Silicon Imaging is proud to continue its innovation in high

The SI-3300 has automatic black **level** calibration, which measures the average value **pixels** from two dark **rows** of the imager for each of the four colors. ...

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array performs may be useful for time-domain **image** processing, for example, First, the **pixel** output must not **change**. during the **reset period**. ...

www.ini.uzh.ch/~tobi/anaprose/retreset/retreset.pdf - Similar pages

Programming the Osram Pictiva OLED display

B2, x, 2, Set **row period**, Bits 0-7 of x set the number of DCLKs per **row**, Note that the **voltage** at which the OLED **pixel** starts to illuminate depends ...

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[PDF] Linear Current-Mode Active Pixel Sensor

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pixel row select switch. The input impedance of the CCI+ "vir- the **image** sensor can be biased with a **reset voltage** closer to ...

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the **row period**, the **image** sensor samples the **pixel voltage** B1SEL This bit changes the **voltage level** of variable **reset** Description of **Change** ...

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suppresses **reset** noise from variations in **reset voltage** between **pixels** Fossum,

CMOS active **pixel image** sensors for highly integrated imaging systems, ...

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global thresholds that are offset from the **reset voltage** to detect increasing and (a)

shows the transistor-level **pixel** schematic corresponding to the ...

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[PDF] [Low Resolution Digital CMOS Image Sensor VISION VV6300](#)

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Pixel reset voltage. 2. VCDS. 1A. **Voltage** reference **change** state in a clock period.

However the gray count sequence is user unfriendly, e.g. 248232233 ...

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The capacitor is **reset**. with Φ_R to a pre-charge level prior to the arrival of the next ... The

voltage change at the source constitutes the video ...

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Shrinkage of **pixel** structures and layouts for CMOS active **pixel image** ... By this, threshold differences between **pixels** do not affect the **reset voltage**. ...

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incident illumination level and the integration period. ... After the **pixel** has been sampled, the **reset** transistor, clocked ...

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[PDF] [A Wide Dynamic Range CMOS Digital Pixel Sensor](#)

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Abstract - A CMOS **image** sensor with **pixel level** analog. to digital conversion is presented.

... mode, all **pixels** are **reset** prior to the integration period. ...

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[Method and circuit for determining the response curve knee point ...](#)

As a result, the two different values—the **reset voltage** V_{rst} and the **image signal voltage**

V_{sig} —are readout from the **pixel** and sent by the column line 170 to ...

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[Design of a Self-Correcting Active Pixel Sensor](#)

to a **voltage level** applied at the line V Pix **Reset** prior to the **voltage** integration of the photocurrent. At the end of an integration period, the row ...

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move signal charge from one **pixel** to the next. **Reset** Clock Φ_R : ...

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